

App. No. 09/746,676

Attorney Docket: 042390.P10141

In The United States Patent and Trademark Office

True Patent Application of:

Miner, *et al.*

Application No.: 09/746,676

Filed: December 22, 2000

For: TEST ACCESS PORT

) Examiner: Chung, Phung M.

) Art Unit: 2133

**DECLARATION OF DAVID E. MINER UNDER 37 C.F.R. § 1.131**

COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

Examiner:

I, David E. Miner, declare the following:

1. I am a co-inventor of the above captioned patent application (hereafter, "the Application") and an inventor of the subject matter described therein.
2. I am an employee for Intel Corporation, the assignee of the Application.
3. At least prior to June 5, 2000, the invention claimed in the Application had been conceived and reduced to practice in this country (the United States of America).
4. Attached is a true copy, in its unredacted form, of the original invention disclosure for this invention, entitled "A Configurable Test Access Port Mechanism for a Multi-Core Architecture." This invention disclosure documents my invention. I and my co-inventors prepared the invention disclosure on or prior to June 5, 2000. The invention disclosure was submitted to my manager, Hang T. Nguyen, for approval. This manager signed the invention

disclosure on June 5, 2000.

The invention disclosure establishes a date of conception of my invention no later than June 5, 2000. This date is earlier than the effective date of the Swamy reference (U.S. Patent No. 6,686,759 B1, November 28, 2000).

5. The invention disclosure was submitted to the Intel legal department for processing according to Intel's normal business practices. The invention disclosure was received by the Intel legal department no later than June 12, 2000.
6. The patent application for this invention was filed with the US Patent and Trademark Office on December 22, 2000.
7. During the period from the date of submission of the invention disclosure to the filing date of December 22, 2000, the invention disclosure was diligently processed by myself and other Intel employees according to the normal business practices of Intel Corporation.
8. I declare, to the best of my knowledge, that all statements made in this document are true, and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under §1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the above-referenced patent application or any patent issued thereon.

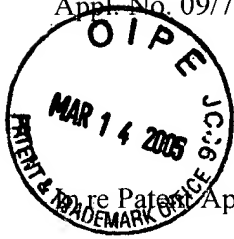
Signed,

1 MARCH 2005

Date:

David E. Miner

Name: David E. Miner



In The United States Patent and Trademark Office

Application of:	)	Examiner:	Chung, Phung M.
Miner, <i>et al.</i>	)	Art Unit:	2133
Application No.: 09/746,676	)		
Filed: December 22, 2000	)		
For: TEST ACCESS PORT	)		

**DECLARATION OF JUSTIN B. SCOUT UNDER 37 C.F.R. § 1.131**

COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

Examiner:

I, Justin B. Scout, declare the following:

1. I am a patent attorney with Intel Corporation, the assignee of the above captioned non-provisional patent application (hereafter, "the Application").
2. Between September and December, 2000 as an employee of the Intel legal department with the title (then) of Patent Engineer, I drafted the Application under the supervision of (then) Intel patent attorney Howard Skaist.
3. In June 2000, the Intel legal department received an invention disclosure that described the invention captured by the Application. Attached is a true copy, in its unredacted form, of the original invention disclosure for this invention, entitled "A Configurable Test Access Port Mechanism for a Multi-Core Architecture."

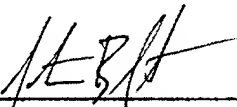
4. The invention disclosure was assigned to a first Intel invention review committee for evaluation. The first committee met to review that invention disclosure, along with many others, in August 2000. At that time, the first committee chose to refer the invention disclosure to a second Intel invention review committee. The process of referring an invention disclosure to a second committee is not atypical and within Intel's standard business practices.
5. The second Intel invention review committee met to review that invention disclosure, along with many others, in September 2000. At that time, the committee chose to file a patent application for the invention described in the above referenced invention disclosure. I understand that the application was assigned to me for drafting on September 20, 2000.
6. After receiving the file, I met with and interviewed the inventors, Messrs. Miner, Tu, and Murray (hereafter, "the inventors"), in early November, 2000.
7. I generated an initial draft of a patent application for the invention described in the invention disclosure on November 5, 2000. This draft was subsequently reviewed and revised by my managing attorney, Howard Skaist. I completed a number of drafts based upon his and the inventors' various revisions. I did not retain a copy of any of these intermediate drafts.
8. I understand that the inventors concluded that the final revision, of December 21, 2000, accurately and completely described their invention. This draft was also approved by my managing attorney.
9. On December 21, 2000, the final revision of the application was submitted to Blakely, Sokoloff, Taylor, & Zafman LLP (BSTZ) in order to complete the administrative work of filing the Application. This was a common Intel business practice at the time. The application was subsequently filed on December 22, 2000.

10. This declaration is being submitted to support a response to an Office Action mailed September 9, 2004 for US Patent Application Serial Number 09/746,676 entitled "Test Access Port."

11. I declare, to the best of my knowledge, that all statements made in this document are true, and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under §1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the above-referenced patent application or any patent issued thereon.

Signed,

Wed Mar 09, 2005  
Date:

  
Name: Justin B. Scout



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Miner, *et al.*

Application No.: 09/746,676

Filed: December 22, 2000

For: TEST ACCESS PORT

) Examiner: Chung, Phung M.

) Art Unit: 2133

**DECLARATION OF STEVEN J. TU UNDER 37 C.F.R. § 1.131**

COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

Examiner:

I, Steven J. Tu, declare the following:

1. I am a co-inventor of the above captioned patent application (hereafter, "the Application") and an inventor of the subject matter described therein.
2. I am an employee for Intel Corporation, the assignee of the Application.
3. At least prior to June 5, 2000, the invention claimed in the Application had been conceived and reduced to practice in this country (the United States of America).
4. Attached is a true copy, in its unredacted form, of the original invention disclosure for this invention, entitled "A Configurable Test Access Port Mechanism for a Multi-Core Architecture." This invention disclosure documents my invention. I and my co-inventors prepared the invention disclosure on or prior to June 5, 2000. The invention disclosure was submitted to my manager, Hang T. Nguyen, for approval. This manager signed the invention

disclosure on June 5, 2000.

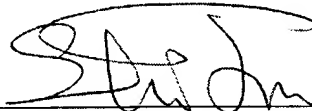
The invention disclosure establishes a date of conception of my invention no later than June 5, 2000. This date is earlier than the effective date of the Swamy reference (U.S. Patent No. 6,686,759 B1, November 28, 2000).

5. The invention disclosure was submitted to the Intel legal department for processing according to Intel's normal business practices. The invention disclosure was received by the Intel legal department no later than June 12, 2000.
6. The patent application for this invention was filed with the US Patent and Trademark Office on December 22, 2000.
7. During the period from the date of submission of the invention disclosure to the filing date of December 22, 2000, the invention disclosure was diligently processed by myself and other Intel employees according to the normal business practices of Intel Corporation.
8. I declare, to the best of my knowledge, that all statements made in this document are true, and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under §1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the above-referenced patent application or any patent issued thereon.

Signed,

Feb. 28, 2005

Date:



Name: Steven J. Tu